

FIG. 1 (PRIOR ART)

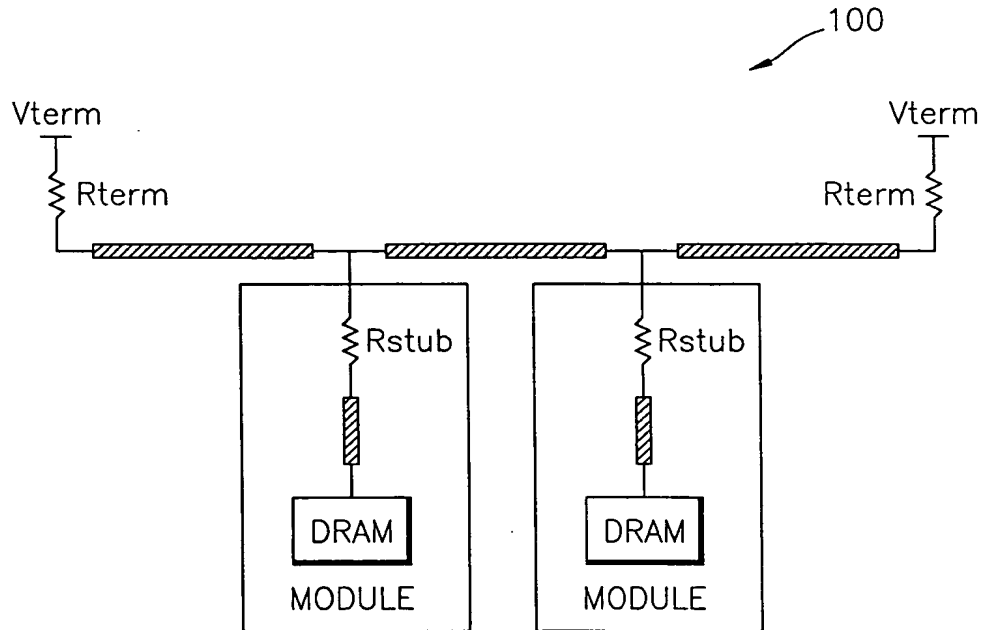
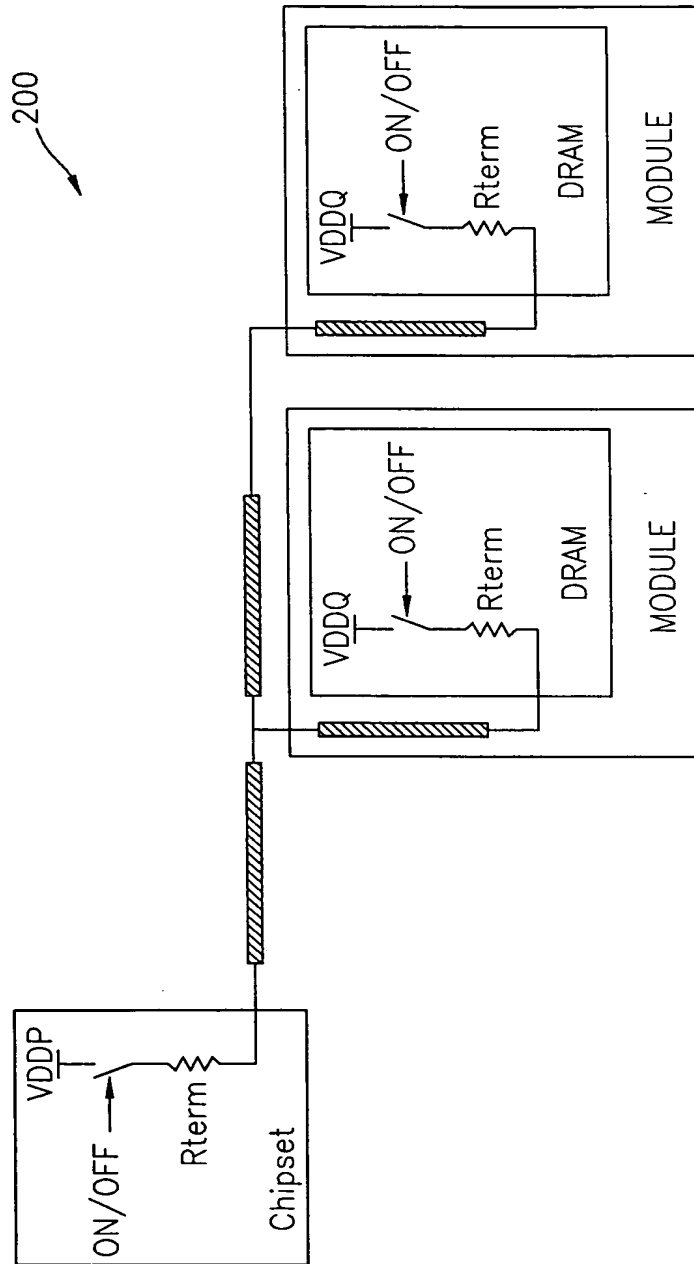


FIG. 2 (PRIOR ART)



DEVICES AND METHODS FOR CONTROLLING ACTIVE TERMINATION RESISTORS IN A
MEMORY SYSTEM

Application No. [NEW] - Attorney Docket No. SEC.924C

Inventor: Kye-Hyun KYUNG

FIG. 3

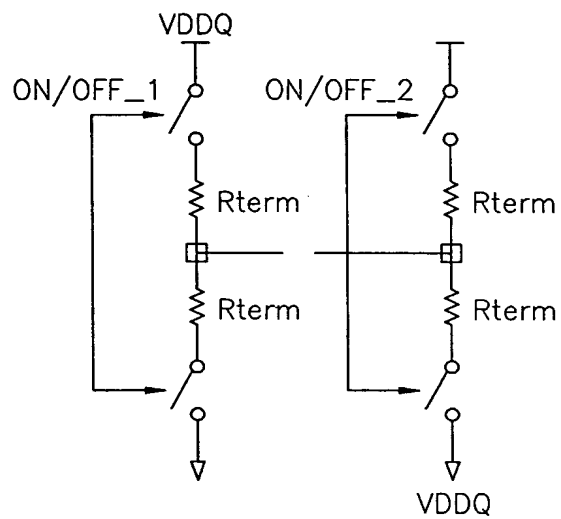


FIG. 4

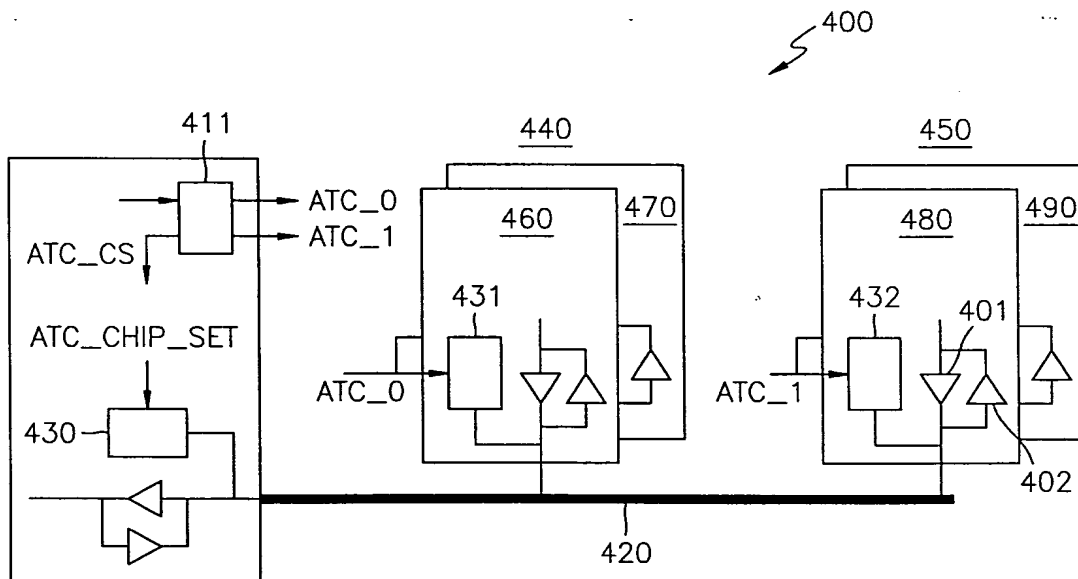


FIG. 5A

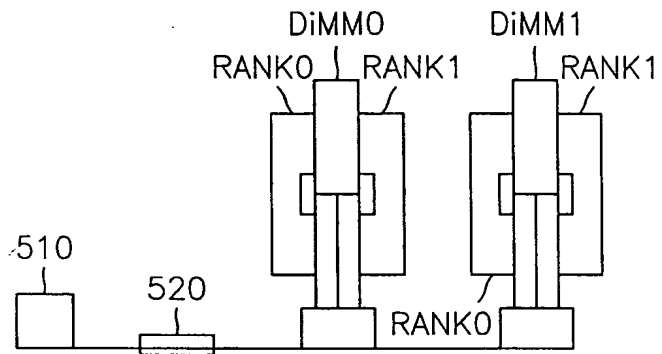
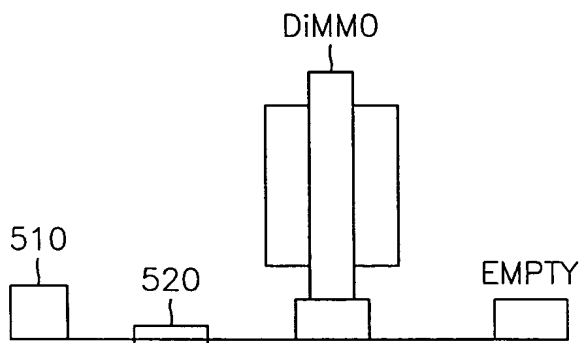


FIG. 5B

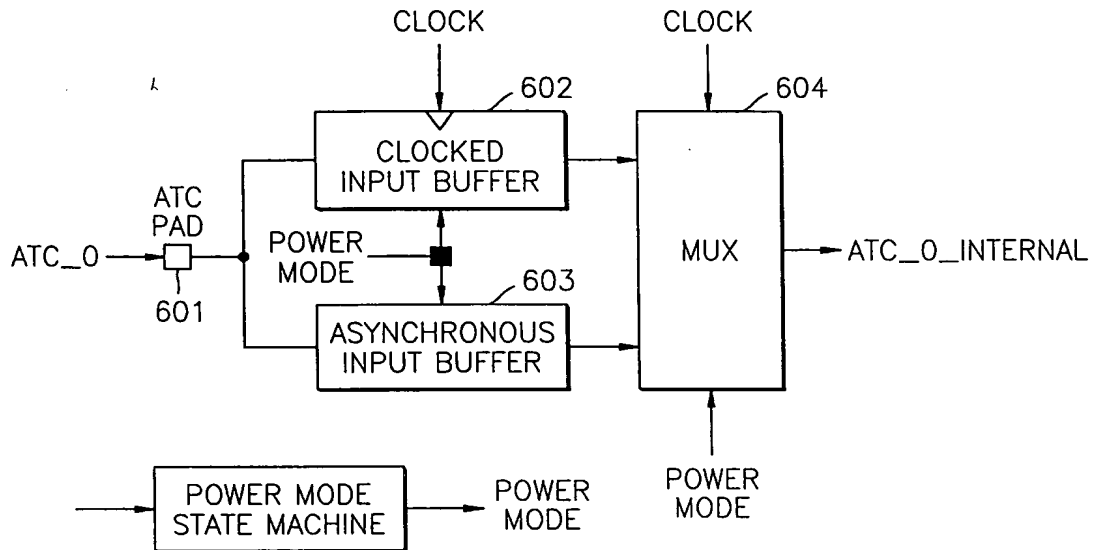


DEVICES AND METHODS FOR CONTROLLING ACTIVE TERMINATION RESISTORS IN A
MEMORY SYSTEM

Application No. [NEW] - Attorney Docket No. SEC.924C

Inventor: Kye-Hyun KYUNG

FIG. 6



DEVICES AND METHODS FOR CONTROLLING ACTIVE TERMINATION RESISTORS IN A
MEMORY SYSTEM

Application No. [NEW] - Attorney Docket No. SEC.924C

Inventor: Kye-Hyun KYUNG

FIG. 7A

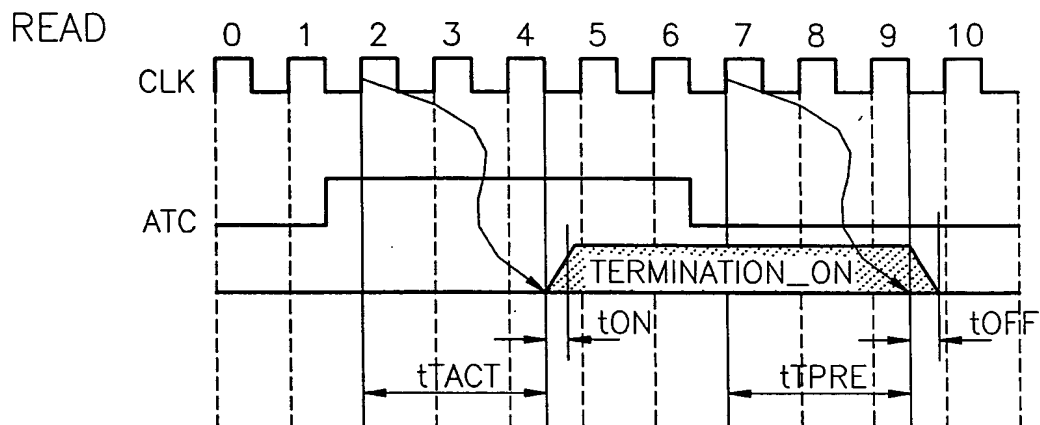
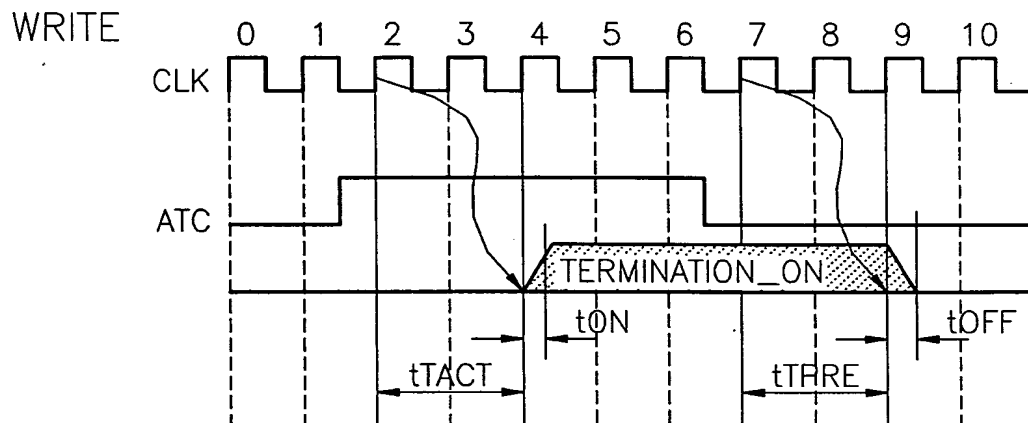


FIG. 7B



DEVICES AND METHODS FOR CONTROLLING ACTIVE TERMINATION RESISTORS IN A
MEMORY SYSTEM

Application No. [NEW] - Attorney Docket No. SEC.924C

Inventor: Kye-Hyun KYUNG

FIG. 8

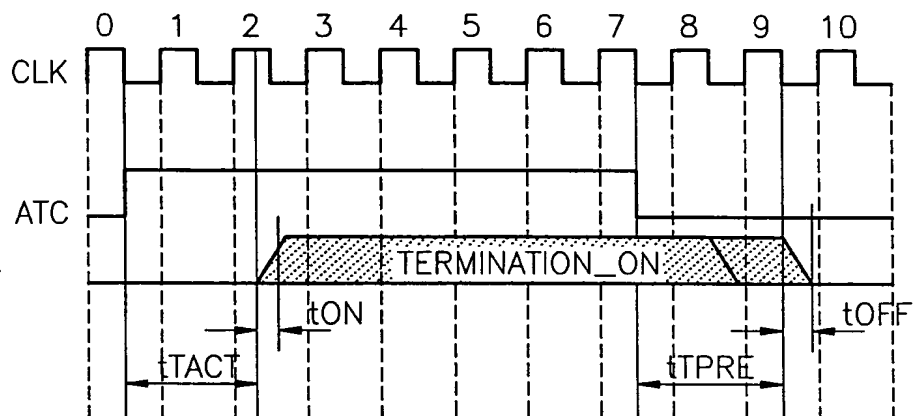


FIG. 9A

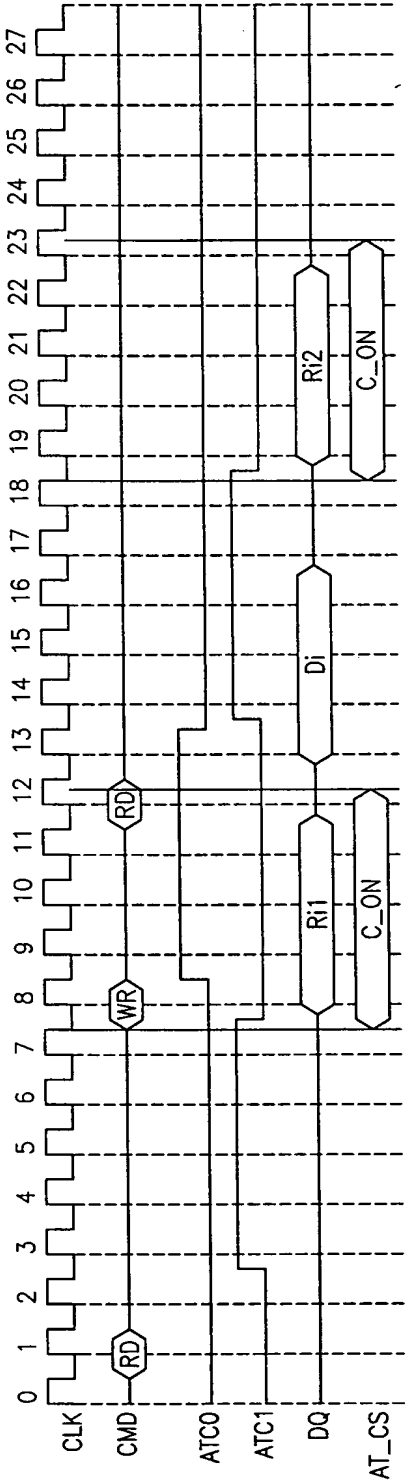
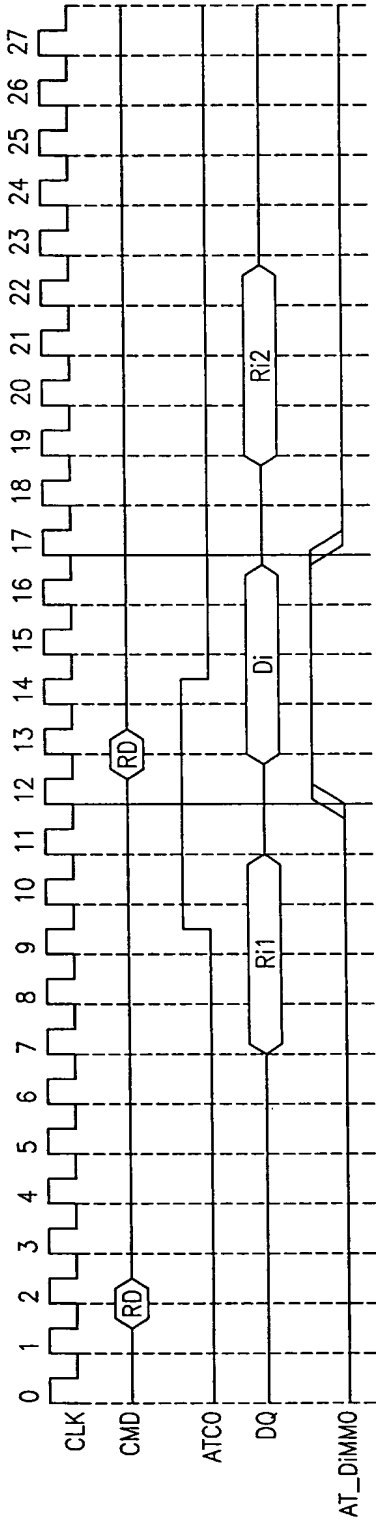


FIG. 9B



DEVICES AND METHODS FOR CONTROLLING ACTIVE TERMINATION RESISTORS IN A
MEMORY SYSTEM

Application No. [NEW] - Attorney Docket No. SEC.924C
Inventor: Kye-Hyun KYUNG

FIG. 9C

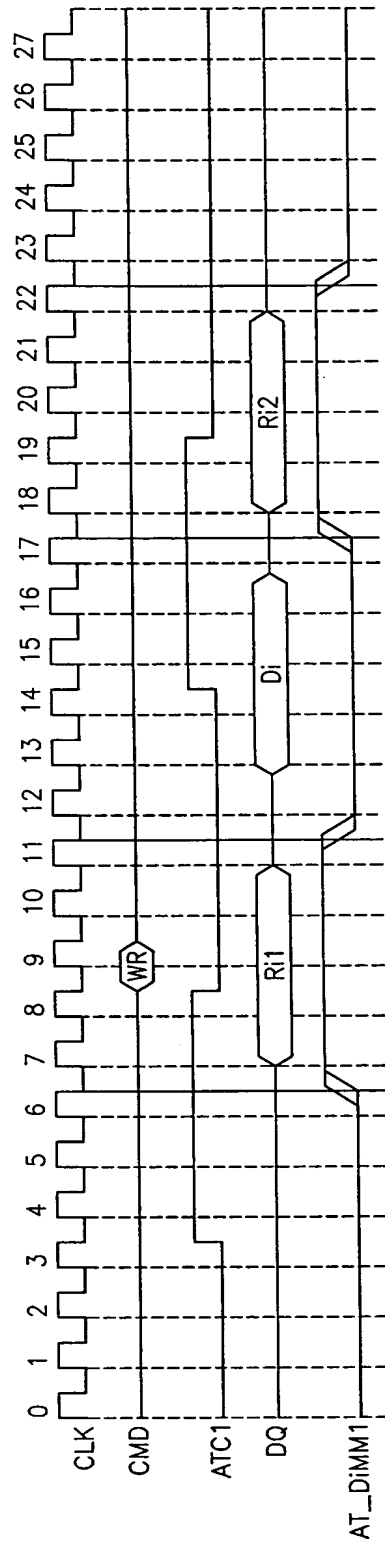


FIG. 10A

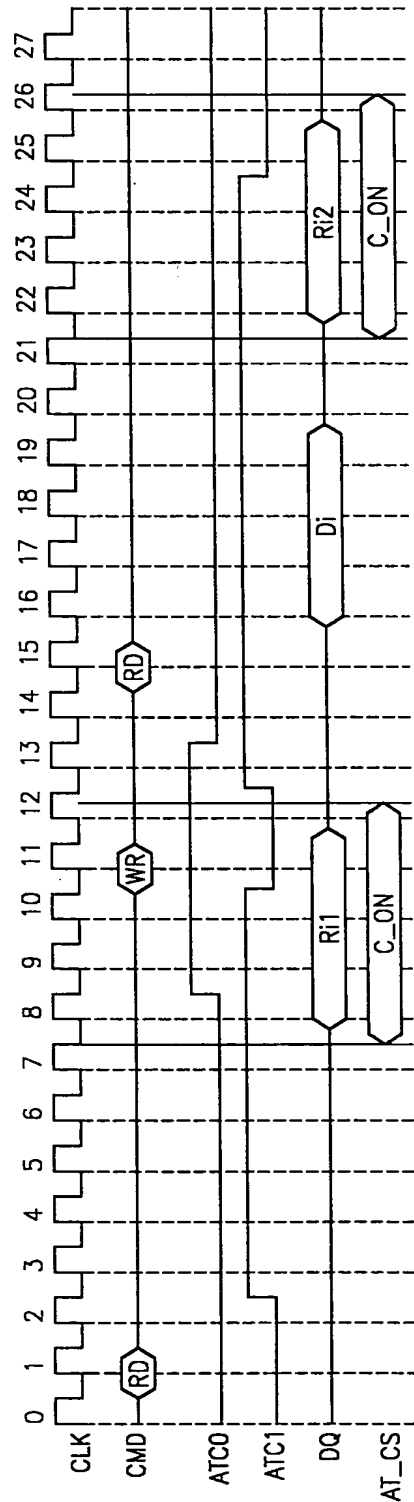
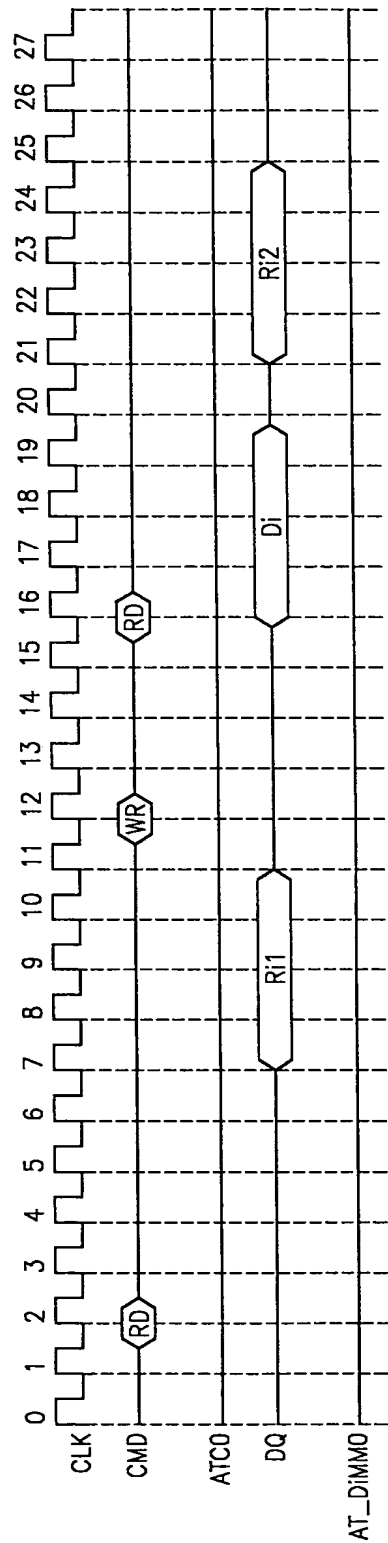


FIG. 10B



DEVICES AND METHODS FOR CONTROLLING ACTIVE TERMINATION RESISTORS IN A
MEMORY SYSTEM

Application No. [NEW] - Attorney Docket No. SEC.924C
Inventor: Kye-Hyun KYUNG

FIG. 10C

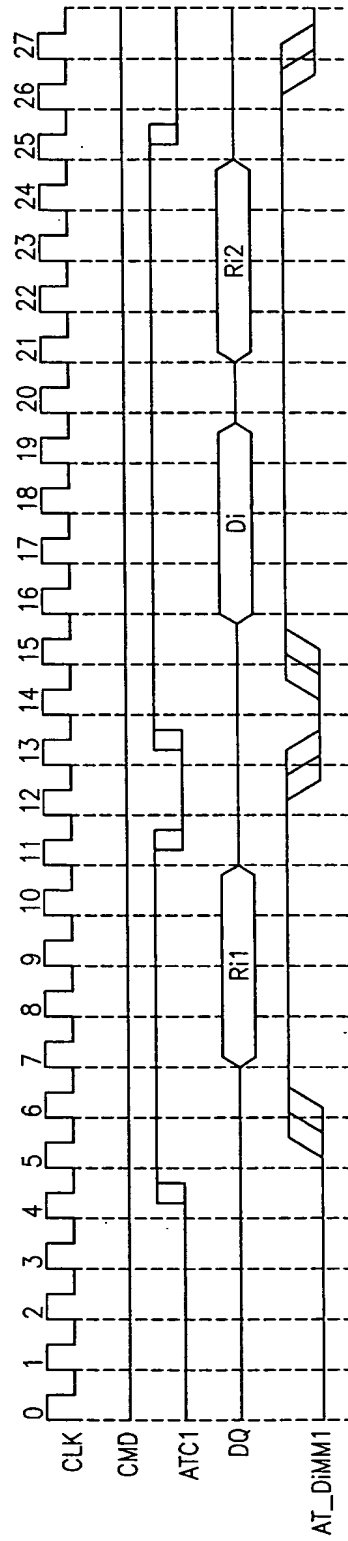


FIG. 11

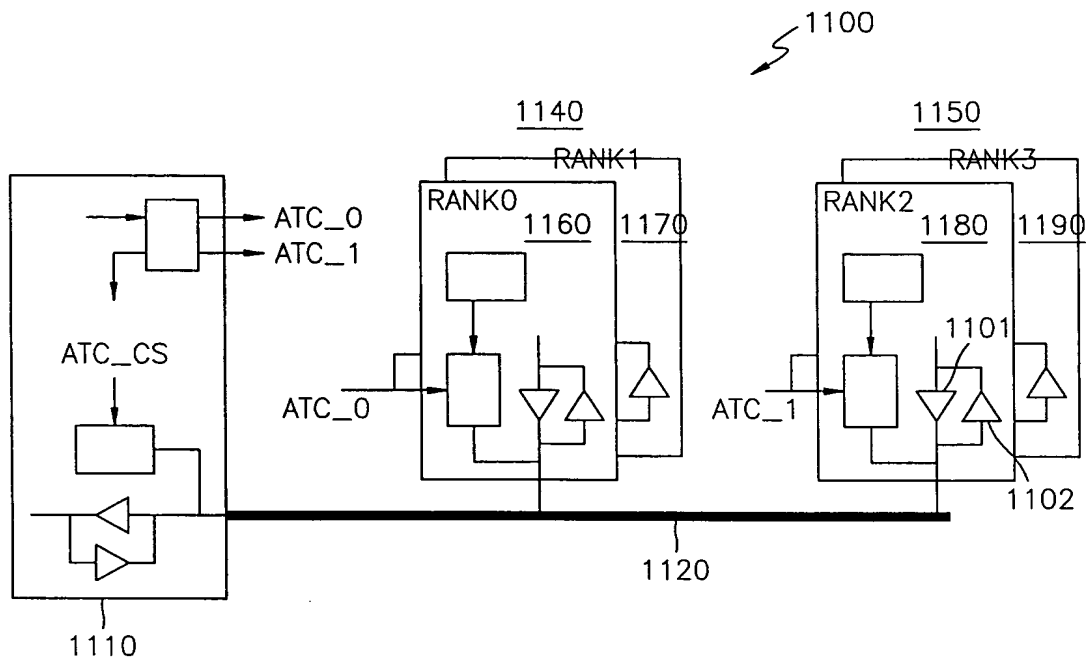


FIG. 12

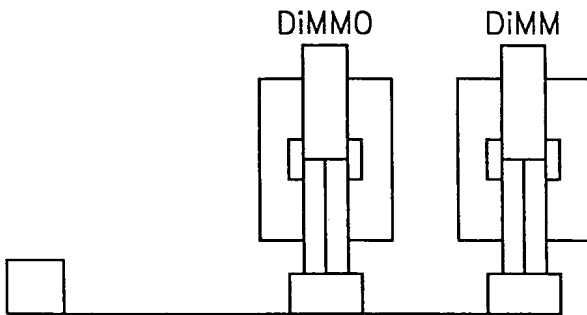


FIG. 13

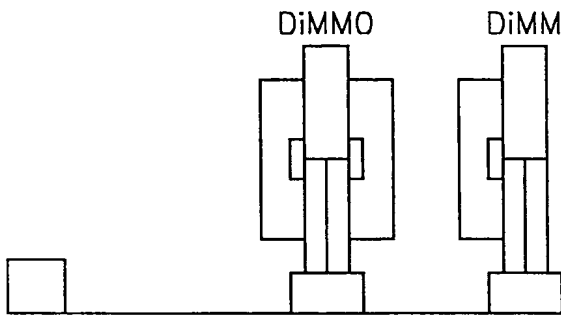


FIG. 14

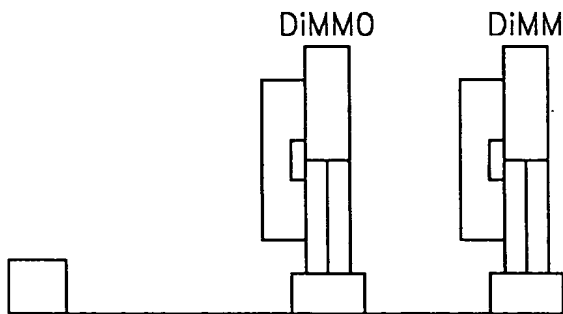


FIG. 15

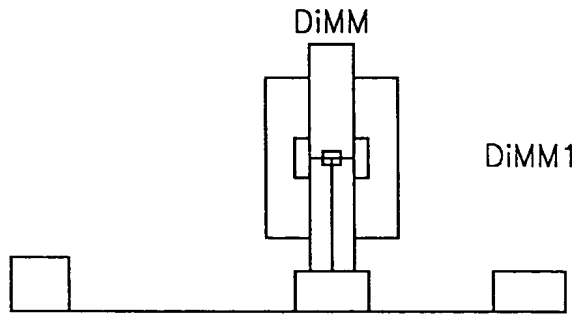


FIG. 16

